

CLAIM AMENDMENTS

1. (Currently Amended) A computer-implemented method of allocating storage to a host processor, comprising:
a control processor receiving a request to allocate storage to the host processor; and
the control processor associating one or more logical units from among one or more storage units to the host processor by:
the control processor configuring a gateway device to map the one or more logical units to the host processor, wherein the gateway device is a physical device;
the control processor configuring the one or more storage units to give the host processor access to the one or more logical units;
~~wherein the host processor does not know which one or more logical units are associated with the host processor;~~
wherein the host processor accesses the one or more logical units, which are associated with the host processor by the control processor, without the host processor knowing which of the one or more logical units from among the one or more storage units are associated with the host processor;
wherein the control processor is a ~~different~~ separate device ~~than~~ from each of the gateway device, the host processor, and the one or more storage units; and
wherein the gateway device is a ~~different~~ separate device ~~than~~ from each of the control processor, the host processor, and the one or more storage units.

2. (Previously Presented) A method as recited in Claim 1, wherein:
the control processor configuring the gateway device and the control processor configuring the one or more storage units are performed by the control processor without modification to an operating system of the host processor;
the gateway device is included in a virtual storage layer;
the host processor and the one or more storage units are included in a virtual server farm;

8 the control processor is coupled through one or more storage networks to a plurality of
9 storage gateways that includes the gateway device; and
10 the plurality of storage gateways are coupled through the storage networks to the one
11 or more storage units.

1 3.-6. (Cancelled)

1 7. (Previously Presented) A method as recited in Claim 1, further comprising:
2 the control processor causing the storage of first information that associates host
3 processors to logical units;
4 the control processor causing the storage of second information that associates logical
5 units to storage units;
6 the control processor associating the one or more logical units from among the one or
7 more storage units to the host processor further comprises the control processor
8 mapping the one or more logical units from among the one or more storage
9 units to a boot port of the host processor by reconfiguring the gateway device
10 to logically couple the one or more logical units to the boot port based on the
11 stored first information and the stored second information;
12 the control processor identifying one or more logical unit numbers corresponding to
13 the one or more logical units;
14 the control processor instructing the gateway device to map the one or more logical
15 unit numbers to the small computer system interface port zero of the host
16 processor based on a unique processor identifier; and
17 the control processor instructing the one or more storage units to give the host
18 processor having the unique host identifier access to the one or more logical
19 unit numbers.

1 8. (Previously Presented) A method as recited in Claim 1, wherein the request to allocate
2 storage to the host processor is a first request to allocate storage to the host processor,
3 and the method further comprises:
4 based on the first request, the control processor generating a second request to allocate
5 storage to the host processor;
6 wherein the control processor is communicatively coupled to a control database;
7 wherein the second request is directed from the control processor to a storage
8 manager;
9 wherein the storage manager is communicatively coupled to the control processor, the
10 control database, and a storage network that includes the gateway device; and
11 the method further comprises the control processor causing the storage manager to
12 issue instructions to the one or more storage units to give the host processor
13 access to the one or more logical units.

1 9. (Cancelled)

1 10. (Previously Presented) A method as recited in Claim 1, wherein the request to allocate
2 storage specifies a first amount of storage, and wherein the control processor
3 associating the one or more logical units further comprises:
4 the control processor identifying the one or more logical units of the one or more
5 storage units that, when combined, have a second amount of storage that is at
6 least as great as the first amount of storage specified in the request.

1 11. (Previously Presented) A method as recited in Claim 1, wherein the request is a first
2 request, and the control processor associating the one or more logical units further
3 comprises:
4 the control processor issuing a second request to allocate one or more volumes on one
5 of the one or more storage units;
6 the control processor issuing a third request to make a concatenated volume using the
7 one or more allocated volumes;
8 the control processor causing the concatenated volume to be configured for use with
9 the host processor;
10 the control processor issuing first instructions to the one or more storage units to bind
11 the host processor to the concatenated volume by giving the host processor
12 access to the concatenated volume;
13 the control processor issuing second instructions to the gateway device to bind the
14 concatenated volume to the host processor.

1 12. (Previously Presented) A method as recited in Claim 11, further comprising:
2 the control processor determining that the second instructions have failed to bind the
3 concatenated volume to the host processor;
4 the control processor issuing third instructions to the one or more storage units to
5 un-bind the host processor from the concatenated volume;
6 the control processor determining that the first instructions have failed to bind the host
7 processor to the concatenated volume; and
8 the control processor issuing fourth instructions to the one or more storage units to
9 break the concatenated volume.

1 13. (Cancelled)

1 14. (Cancelled)

1 15. (Previously Presented) A method as recited in Claim 1, wherein:
2 the one or more logical units associated with the host processor include at least a first
3 logical unit from a first volume of a first storage unit of the one or more
4 storage units and at least a second logical unit from a second volume of a
5 second storage unit of the one or more storage units;
6 the request to allocate storage specifies a parameter selected from the group consisting
7 of an amount of storage to be allocated and a type of storage to be allocated;
8 the control processor is separate from the gateway device, the host processor, and the
9 one or more storage units; and
10 the gateway device is separate from the control processor, the host processor, and the
11 one or more storage units.

1 16.-39. (Cancelled)

1 40. (Currently Amended) A computer-readable medium for allocating storage to a host
2 processor, the computer-readable medium carrying one or more sequences of
3 instructions which, when executed by one or more processors, cause the one or more
4 processors to carry out the steps of:
5 a control processor receiving a request to allocate storage to the host processor; and
6 the control processor associating one or more logical units from among one or more
7 storage units to the host processor by:
8 the control processor configuring a gateway device to map the one or more
9 logical units to the host processor, wherein the gateway device is a
10 physical device;[:]
11 the control processor configuring the one or more storage units to give the host
12 processor access to the one or more logical units;
13 ~~wherein the host processor does not know which one or more logical units are~~
14 ~~associated with the host processor;~~

15 wherein the host processor accesses the one or more logical units, which are associated
16 with the host processor by the control processor, without the host processor
17 knowing which of the one or more logical units from among the one or more
18 storage units are associated with the host processor;

19 wherein the control processor is ~~different~~ a separate device than from each of the
20 gateway device, the host processor, and the one or more storage units; and

21 wherein the gateway device is ~~different~~ a separate device than from each of the control
22 processor, the host processor, and the one or more storage units.

1 41. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein:
2 the control processor configuring the gateway device and the control processor
3 configuring the one or more storage units are performed by the control
4 processor without modification to an operating system of the host processor;
5 the gateway device is included in a virtual storage layer;
6 the host processor and the one or more storage units are included in a virtual server
7 farm;
8 the control processor is coupled through one or more storage networks to a plurality of
9 storage gateways that includes the gateway device; and
10 the plurality of storage gateways are coupled through the storage networks to the one
11 or more storage units.

1 42. (Previously Presented) A computer-readable medium as recited in Claim 40, further
2 comprising one or more sequences of instructions which, when executed by the control
3 one or more processors, cause the one or more processors to carry out the steps of:
4 the control processor causing the storage of first information that associates host
5 processors to logical units;
6 the control processor causing the storage of second information that associates logical
7 units to storage units;
8 the instructions for the control processor associating the one or more logical units from
9 among the one or more storage units to the host processor further comprise one
10 or more sequences of instructions which, when executed by the one or more

11 processors, cause the one or more processors to carry out the step of the control
12 processor mapping the one or more logical units from among the one or more
13 storage units to a boot port of the host processor by reconfiguring the gateway
14 device to logically couple the one or more logical units to the boot port based
15 on the stored first information and the stored second information;
16 the control processor identifying one or more logical unit numbers corresponding to
17 the one or more logical units;
18 the control processor instructing the gateway device to map the one or more logical
19 unit numbers to the small computer system interface port zero of the host
20 processor based on a unique processor identifier; and
21 the control processor instructing the one or more storage units to give the host
22 processor having the unique host identifier access to the one or more logical
23 unit numbers.

1 43. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein
2 the request to allocate storage to the host processor is a first request to allocate storage
3 to the host processor, and the computer-readable medium further comprises one or
4 more sequences of instructions which, when executed by the one or more processors,
5 cause the one or more processors to carry out the step of:
6 based on the first request, the control processor generating a second request to allocate
7 storage to the host processor;
8 wherein the control processor is communicatively coupled to a control database;
9 wherein the second request is directed from the control processor to a storage
10 manager;
11 wherein the storage manager is communicatively coupled to the control processor, the
12 control database, and a storage network that includes the gateway device; and
13 the computer-readable medium further comprises one or more sequences of
14 instructions which, when executed by the one or more processors, cause the
15 one or more processors to carry out the step of the control processor causing
16 the storage manager to issue instructions to the one or more storage units to
17 give the host processor access to the one or more logical units.

1 44. (Cancelled)

1 45. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein
2 the request to allocate storage specifies a first amount of storage, and wherein the one
3 or more sequences of instructions for the control processor associating the one or more
4 logical units further comprise one or more sequences of instructions which, when
5 executed by the one or more processors, cause the one or more processors to carry out
6 the steps of:
7 the control processor identifying the one or more logical units of the one or more
8 storage units that, when combined, have a second amount of storage that is at
9 least as great as the first amount of storage specified in the request.

1 46. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein
2 the request is a first request, and the instructions for the control processor associating
3 the one or more logical units further comprise one or more sequences of instructions
4 which, when executed by the one or more processors, cause the one or more
5 processors to carry out the steps of:
6 the control processor issuing a second request to allocate one or more volumes on one
7 of the one or more storage units;
8 the control processor issuing a third request to make a concatenated volume using the
9 one or more allocated volumes;
10 the control processor causing the concatenated volume to be configured for use with
11 the host processor;
12 the control processor issuing first instructions to the one or more storage units to bind
13 the host processor to the concatenated volume by giving the host processor
14 access to the concatenated volume;
15 the control processor issuing second instructions to the gateway device to bind the
16 concatenated volume to the host processor.

1 47. (Previously Presented) A computer-readable medium as recited in Claim 46, further
2 comprising one or more sequences of instructions which, when executed by the one or
3 more processors, cause the one or more processors to carry out the steps of:
4 the control processor determining that the second instructions have failed to bind the
5 concatenated volume to the host processor;
6 the control processor issuing third instructions to the one or more storage units to
7 un-bind the host processor from the concatenated volume;
8 the control processor determining that the first instructions have failed to bind the host
9 processor to the concatenated volume; and
10 the control processor issuing fourth instructions to the one or more storage units to
11 break the concatenated volume.

1 48. (Cancelled)

1 49. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein:
2 the one or more logical units associated with the host processor include at least a first
3 logical unit from a first volume of a first storage unit of the one or more
4 storage units and at least a second logical unit from a second volume of a
5 second storage unit of the one or more storage units;
6 the request to allocate storage specifies a parameter selected from the group consisting
7 of an amount of storage to be allocated and a type of storage to be allocated;
8 the control processor is separate from the gateway device, the host processor, and the
9 one or more storage units; and
10 the gateway device is separate from the control processor, the host processor, and the
11 one or more storage units.

1 50. (Currently Amended) An apparatus for allocating storage to a host processor, the
2 apparatus comprising a control processor that is configured to carry out the steps of:
3 receiving a request to allocate storage to the host processor; and

4 associating one or more logical units from among one or more storage units to the host
5 processor by:
6 configuring a gateway device to map the one or more logical units to the host
7 processor, wherein the gateway device is a physical device;[:]
8 configuring the one or more storage units to give the host processor access to
9 the one or more logical units;
10 ~~wherein the host processor does not know which one or more logical units are~~
11 ~~associated with the host processor;~~
12 wherein the host processor accesses the one or more logical units, which are associated
13 with the host processor by the control processor, without the host processor
14 knowing which of the one or more logical units from among the one or more
15 storage units are associated with the host processor;
16 wherein the control processor is ~~different~~ a separate device from each of the gateway
17 device, the host processor, and the one or more storage units; and
18 wherein the gateway device is ~~different~~ a separate device from each of the control
19 processor, the host processor, and the one or more storage units.

1 51. (Previously Presented) An apparatus as recited in Claim 50, wherein:
2 configuring the gateway device and configuring the one or more storage units are
3 performed by the control processor without modification to an operating
4 system of the host processor;
5 the gateway device is included in a virtual storage layer;
6 the host processor and the one or more storage units are included in a virtual server
7 farm;
8 the control processor is coupled through one or more storage networks to a plurality of
9 storage gateways that includes the gateway device; and
10 the plurality of storage gateways are coupled through the storage networks to the one
11 or more storage units.

1 52. (Previously Presented) An apparatus as recited in Claim 50, wherein the control
2 processor is further configured to carry out the steps of:
3 causing the storage of first information that associates processors to logical units;
4 causing the storage of second information that associates logical units to storage units;
5 wherein the control processor being configured for associating the one or more logical
6 units from among the one or more storage units to the host processor further
7 comprises configuring the control processor to carry out the step of mapping
8 the one or more logical units from among the one or more storage units to a
9 boot port of the host processor by reconfiguring the gateway device to logically
10 couple the one or more logical units to the boot port based on the stored first
11 information and the stored second information;
12 the control processor identifying one or more logical unit numbers corresponding to
13 the one or more logical units;
14 the control processor instructing the gateway device to map the one or more logical
15 unit numbers to the small computer system interface port zero of the host
16 processor based on a unique processor identifier; and
17 the control processor instructing the one or more storage units to give the host
18 processor having the unique host identifier access to the one or more logical
19 unit numbers.

1 53. (Previously Presented) An apparatus as recited in Claim 50, wherein the request to
2 allocate storage to the host processor is a first request to allocate storage to the host
3 processor, and wherein the control processor is further configured to carry out the step
4 of:
5 generating a second request to allocate storage to the host processor, based on the first
6 request;
7 wherein the control processor is communicatively coupled to a control database;

8 wherein the second request is directed from the control processor to a storage
9 manager; wherein the storage manager is communicatively coupled to the
10 control processor, the control database, and a storage network that includes the
11 gateway device;
12 the control processor is further configured to carry out the step of causing the storage
13 manager to issue instructions to the one or more storage units to give the host
14 processor access to the one or more logical units.

1 54. (Cancelled)

1 55. (Previously Presented) An apparatus as recited in Claim 50, wherein the request to
2 allocate storage specifies a first amount of storage, and wherein the control processing
3 being configured for associating the one or more logical units further comprises
4 configuring the control processor to carry out the steps of:
5 the control processor identifying the one or more logical units of the one or more
6 storage units that, when combined, have a second amount of storage that is at
7 least as great as the first amount of storage specified in the request .

1 56. (Previously Presented) An apparatus as recited in Claim 50, wherein the request is a
2 first request, and configuring the control processor for associating the one or more
3 logical units further comprises configuring the control processor to carry out the steps
4 of:
5 issuing a second request to allocate one or more volumes on one of the one or more
6 storage units;
7 issuing a third request to make a concatenated volume using the one or more allocated
8 volumes;
9 causing the concatenated volume to be configured for use with the host processor;
10 issuing first instructions to the one or more storage units to bind the host processor to
11 the concatenated volume by giving the host processor access to the
12 concatenated volume;

13 issuing second instructions to the gateway device to bind the concatenated volume to
14 the host processor.

1 57. (Previously Presented) An apparatus as recited in Claim 56, wherein the control
2 processor is further configured to carry out the steps of:
3 determining that the second instructions have failed to bind the concatenated volume
4 to the host processor;
5 issuing third instructions to the one or more storage units to un-bind the host processor
6 from the concatenated volume;
7 determining that the first instructions have failed to bind the host processor to the
8 concatenated volume; and
9 issuing fourth instructions to the one or more storage units to break the concatenated
10 volume.

1 58. (Cancelled)

1 59. (Previously Presented) An apparatus as recited in Claim 50, wherein:
2 the one or more logical units associated with the host processor include at least a first
3 logical unit from a first volume of a first storage unit of the one or more
4 storage units and at least a second logical unit from a second volume of a
5 second storage unit of the one or more storage units;
6 the request to allocate storage specifies a parameter selected from the group consisting
7 of an amount of storage to be allocated and a type of storage to be allocated;
8 the control processor is separate from the gateway device, the host processor, and the
9 one or more storage units; and
10 the gateway device is separate from the control processor, the host processor, and the
11 one or more storage units.

1 60. (Currently Amended) A method as recited in Claim 1, wherein the host processor
2 does not ~~identify~~ determine which one or more logical units are associated with the
3 host processor.

1 61. (Previously Presented) A method as recited in Claim 1, wherein:
2 the one or more logical units are associated with one or more logical unit numbers; and
3 the host processor does not know the one or more logical unit numbers for the one or
4 more logical units that are associated with the host processor.

1 62. (Previously Presented) A method as recited in Claim 1, wherein:
2 the host processor is a first host processor;
3 the one or more logical units include a first logical unit and a second logical unit;
4 the one or more storage units include a first storage unit and a second storage unit;
5 the first logical unit is associated with the first storage unit;
6 the second logical unit is associated with the second storage unit;
7 the control processor associates the first logical unit and the second logical unit to the
8 first host processor at a first time; and
9 the method further comprises:
10 at a second time that is after the first time, the control processor associating the second
11 logical unit with a second host processor by:
12 the control processor configuring the gateway device to map the second logical
13 unit to the second host processor instead of the first host processor;
14 the control processor configuring the second storage unit to give the second
15 host processor access to the second logical unit instead of the first host
16 processor;
17 wherein the second host processor does not determine that the second logical
18 unit is associated with the second host processor;
19 wherein the first logical unit remains associated with the first host processor;
20 at a third time that is after the second time, the control processor associating the
21 second logical unit with the first host processor by:
22 the control processor configuring the gateway device to map the second logical
23 unit to the first host processor instead of the second host processor;

24 the control processor configuring the second storage unit to give the first host
25 processor access to the second logical unit instead of the second host
26 processor;
27 wherein the first host processor does not determine that the second logical unit
28 wherein the first host processor does not determine that the second
29 logical unit is associated with the first host processor; and
30 wherein the first logical unit remains associated with the first host processor.

1 63. (Currently Amended) A computer-readable medium as recited in Claim 40, wherein
2 the host processor does not ~~identify~~ determine which one or more logical units are
3 associated with the host processor.

1 64. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein:
2 the one or more logical units are associated with one or more logical unit numbers; and
3 the host processor does not know the one or more logical unit numbers for the one or
4 more logical units that are associated with the host processor.

1 65. (Previously Presented) A computer-readable medium as recited in Claim 40, wherein:
2 the host processor is a first host processor;
3 the one or more logical units include a first logical unit and a second logical unit;
4 the one or more storage units include a first storage unit and a second storage unit;
5 the first logical unit is associated with the first storage unit;
6 the second logical unit is associated with the second storage unit;
7 the control processor associates the first logical unit and the second logical unit to the
8 first host processor at a first time; and
9 the computer-readable medium further comprises one or more sequences of
10 instructions which, when executed by the control one or more processors,
11 cause the one or more processors to carry out the steps of:
12 at a second time that is after the first time, the control processor associating the second
13 logical unit with a second host processor by:

14 the control processor configuring the gateway device to map the second logical
15 unit to the second host processor instead of the first host processor;
16 the control processor configuring the second storage unit to give the second
17 host processor access to the second logical unit instead of the first host
18 processor;
19 wherein the second host processor does not determine that the second logical
20 unit is associated with the second host processor;
21 wherein the first logical unit remains associated with the first host processor;
22 at a third time that is after the second time, the control processor associating the
23 second logical unit with the first host processor by:
24 the control processor configuring the gateway device to map the second logical
25 unit to the first host processor instead of the second host processor;
26 the control processor configuring the second storage unit to give the first host
27 processor access to the second logical unit instead of the second host
28 processor;
29 wherein the first host processor does not determine that the second logical unit
30 wherein the first host processor does not determine that the second
31 logical unit is associated with the first host processor; and
32 wherein the first logical unit remains associated with the first host processor.

1 66. (Currently Amended) An apparatus as recited in Claim 50, wherein the host processor
2 does not ~~identify~~ determine which one or more logical units are associated with the
3 host processor.

1 67. (Previously Presented) An apparatus as recited in Claim 50, wherein:
2 the one or more logical units are associated with one or more logical unit numbers; and
3 the host processor does not know the one or more logical unit numbers for the one or
4 more logical units that are associated with the host processor.

1 68. (Previously Presented) An apparatus as recited in Claim 50, wherein:
2 the host processor is a first host processor;

3 the one or more logical units include a first logical unit and a second logical unit;
4 the one or more storage units include a first storage unit and a second storage unit;
5 the first logical unit is associated with the first storage unit;
6 the second logical unit is associated with the second storage unit;
7 the control processor associates the first logical unit and the second logical unit to the
8 first host processor at a first time; and
9 the control processor is further configured to carry out the steps of:
10 at a second time that is after the first time, associating the second logical unit with a
11 second host processor by:
12 configuring the gateway device to map the second logical unit to the second
13 host processor instead of the first host processor;
14 configuring the second storage unit to give the second host processor access to
15 the second logical unit instead of the first host processor;
16 wherein the second host processor does not determine that the second logical
17 unit is associated with the second host processor;
18 wherein the first logical unit remains associated with the first host processor;
19 at a third time that is after the second time, associating the second logical unit with the
20 first host processor by:
21 configuring the gateway device to map the second logical unit to the first host
22 processor instead of the second host processor;
23 configuring the second storage unit to give the first host processor access to the
24 second logical unit instead of the second host processor;
25 wherein the first host processor does not determine that the second logical unit
26 is associated with the first host processor; and
27 wherein the first logical unit remains associated with the first host processor.